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5-6-04

UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/905,002
Filed: July 12, 2001
Inventor(s):
Hughes, William A.

Examiner: Namazi, M.
Group/Art Unit: 2188
Atty. Dkt. No: 5500-69700

Title: PARTIAL DIRECTORY
CACHE FOR REDUCING
PROBE TRAFFIC IN
MULTIPROCESSOR
SYSTEMS

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Erik A. Heter
Principal Name
Signature Date 4/23/04

AMENDMENT; RESPONSE TO OFFICE ACTION OF
January 20, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of January 20, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

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APR 28 2004

Technology Center 2100